

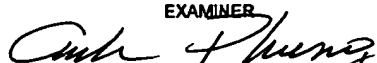


INFORMATION DISCLOSURE CITATION IN AN APPLICATION  (PTO-1449)		ATTY. DOCKET NO. 67161-039	SERIAL NO. 10/615,777
APPLICANT Hideto HIDAKA		FILING DATE July 10, 2003	GROUP 2824

U.S. PATENT DOCUMENTS					
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code(s) (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US 6,577,527 B2	6/10/2003	Freitag et al.	
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FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes - Number - Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

EXAMINER 	DATE CONSIDERED 6/20/05
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SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 67161-039	SERIAL NO. 10/615,777		
				APPLICANT Hideto HIDAKA			
				FILING DATE July 10, 2003	GROUP 2811		
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Documt Number Number-Kind Code(s) (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
ap	A1	US 20020172073	11/21/2002	HIDAKA	Not considered ap 11/26/2004		
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ap		JP 2002-170375	08/14/2002	HIRAI		Yes	No
						abstract	
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
ap ap ap		SCHEUERLEIN, et al. "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in Each Cell" IEEE International Solid-State Circuits Conference (2000) TA 7.2					
		DURLAM, et al. "Nonvolatile RAM Based on Magnetic Tunnel Junction Elements" IEEE International Solid-State Circuits Conference (2000) TA 7.3					
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EXAMINER Cath Phung				DATE CONSIDERED 11/26/2004 6/10/05 ap			

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